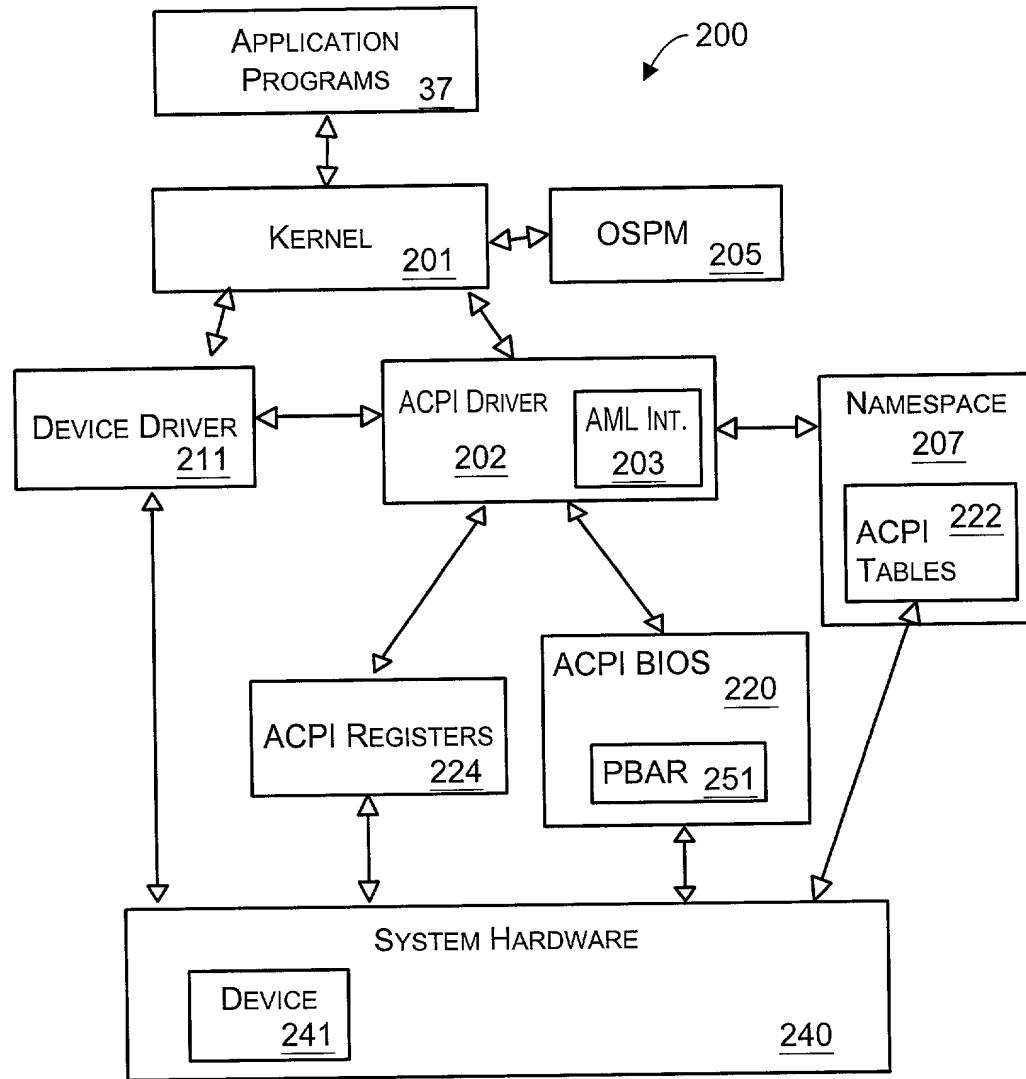
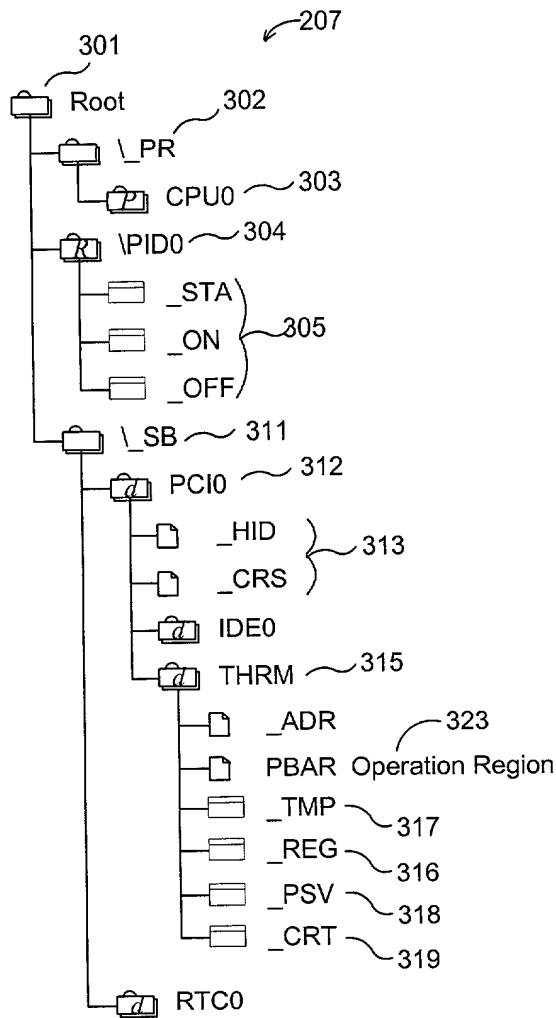


**FIG. 1**



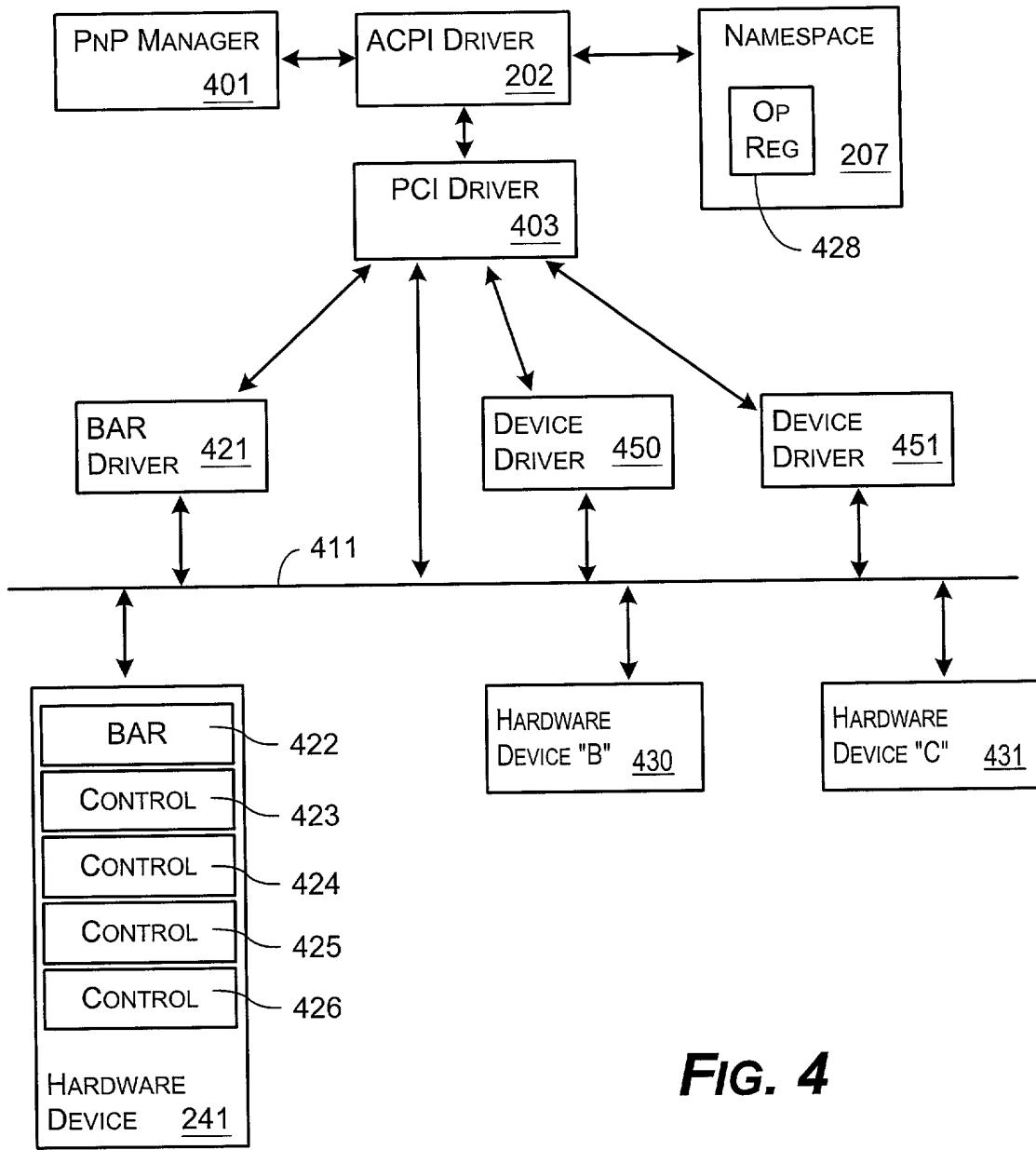
**FIG. 2**



- Processor Tree
- Processor 0 object
- Power resource for IDE0
- Method to return status of power resource
- Method to turn on power resource
- Method to turn off power resource
- System bus tree
- PCI bus
- Device ID
- Current resources (PCI bus number)
- IDE0 device
- PCI object (Thermal Sensor)**
- Object to Define the Device's Address**
- PCI BAR Operation Region**
- Method to return current temperature**
- Method to change availability of Op Reg**
- Method to return passive trip point**
- Method to declare the critical temperature**
- CMOS object

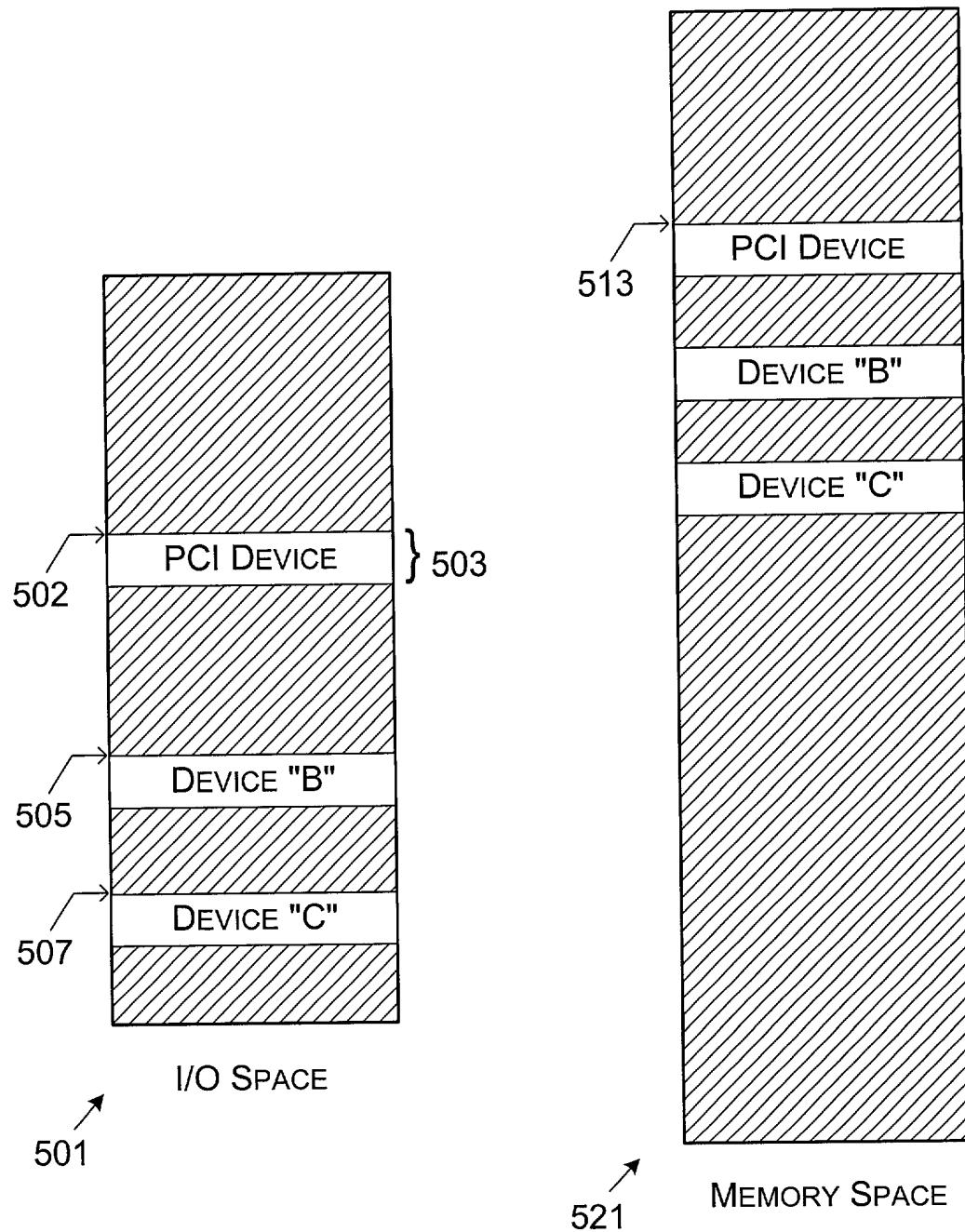
**FIG. 3**

Key	
	Package
	Power Resource Object
	Bus/Device Object
	Data Object
	Control Method (AML code)

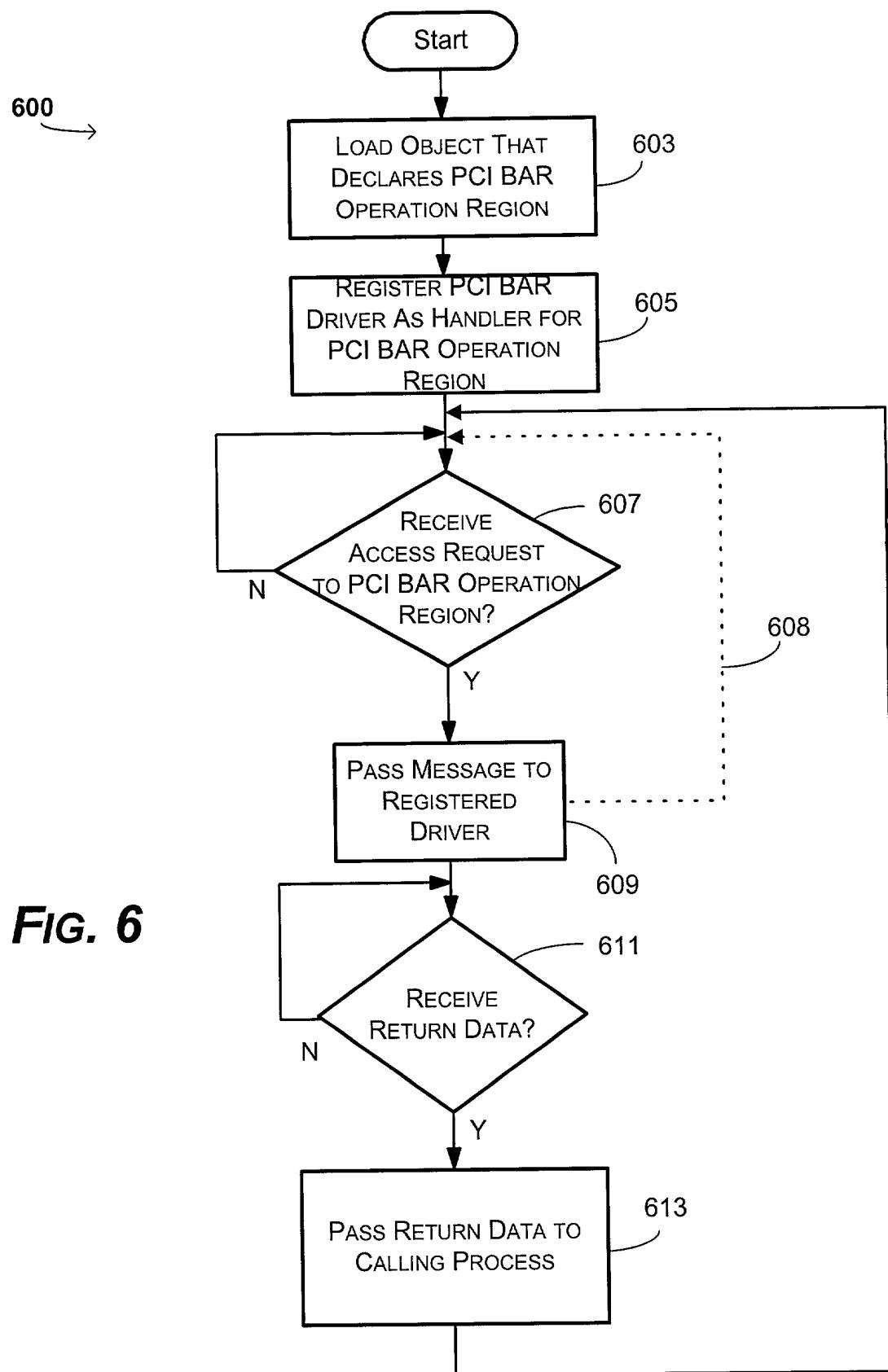


**FIG. 4**

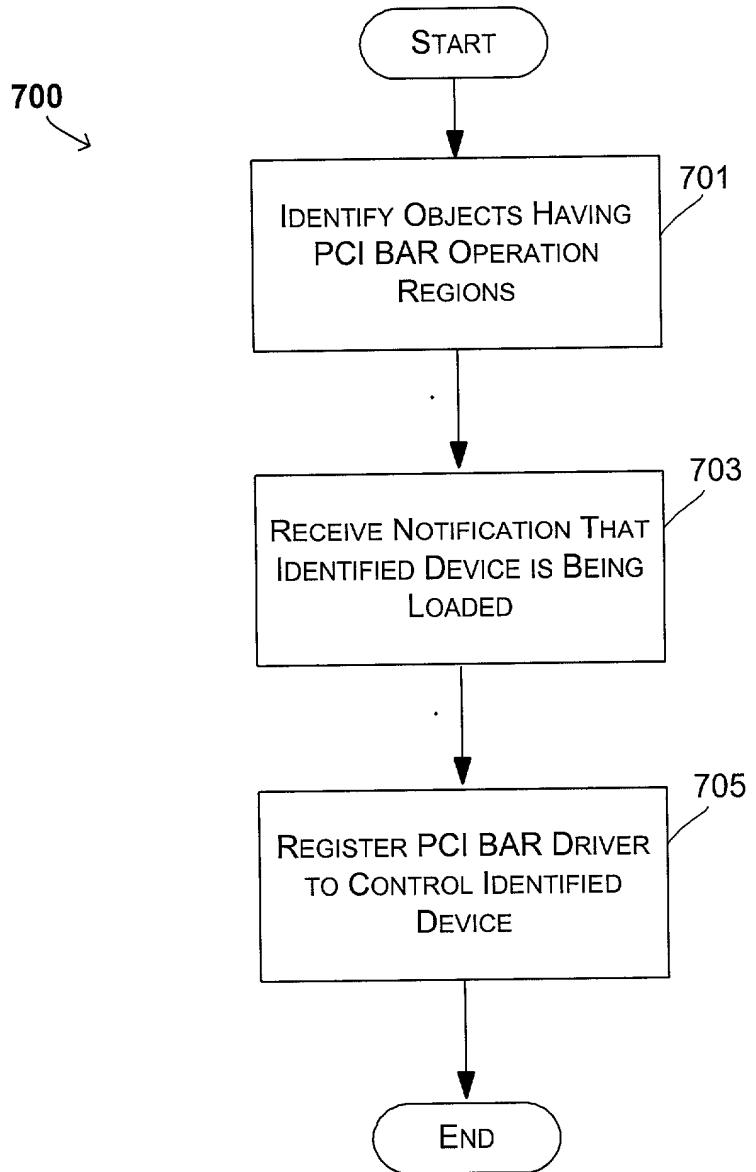
7035760 \* 555555555555



**FIG. 5**



**FIG. 6**



**FIG. 7**